

Applicant : Hisashi Ohtani  
Serial No. : 09/389,393  
Filed : September 3, 1999  
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Attorney's Docket No.: 07977-204002 / US3480D1

Amendments to the Drawings:

Please delete FIG. 5 in its entirety. Since the figure is being deleted, and since it is the only figure on its sheet, an annotated or replacement drawing sheet is not included.

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REMARKS

Claims 1, 44-45, 47-48, 55-57, 59-63, 65-69, 71-75, 77-81, 83, and 84 are pending in the application, with claims 1, 44, 45, 47 and 48 being independent. Claims 2-43, 46, 49-54, 58, 64, 70, 76, and 82 have been cancelled, or were previously cancelled. Claims 1, 44, 45, 47, 48, 55-57, 59, 60, 73-75, 77, and 78 have been amended. FIG. 5 has been deleted in its entirety. No new matter has been added.

Applicant thanks the Examiner for the telephone conversation with the undersigned Attorney of March 9, 2005. In accordance with the Examiner's suggestions, Applicant has deleted FIG. 5, and amended independent claims 1, 44, 45, 47, and 48 so as to reflect the corresponding claims prior to their amendments in the responses dated November 3, 2003 and February 4, 2004, except that each independent claim 1, 44, 45, 47, and 48 now recites, "...wherein the semiconductor device forms a bottom gate type transistor in which the first conductive layer and the second conductive layer are located below the semiconductor region." Since the previously-filed claims were not rejected under 35 U.S.C. 112, first paragraph, and since Applicant's specification clearly supports the above-quoted claim language, by, for example, the language of page 14, lines 5-14, Applicant submits that the present claims are described, enable, and supported under 35 U.S.C. 112, first paragraph.

Claims 55-57, 59, and 60 have been amended to correct typographical errors. Claims 73-75, 77, and 78 have been amended to conform to their respective amended independent claims 1, 44, 45, 47, and 48.

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Respectfully submitted,

Date: March 17, 2005

  
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William G. Hughes, Jr.  
Reg. No. 46,112

Fish & Richardson P.C.  
1425 K Street, N.W.  
11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331

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